

X-916 US

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PATENT  
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IN THE UNITED STATES PATENT OFFICE

Applicant: Nigel G. Herron et al.

Assignee: Xilinx, Inc.

Title: "Method and Apparatus for Testing Circuitry Embedded Within a Field Programmable Gate Array"

Serial No.: 09/991,412

File Date: 11/16/2001

Examiner: David Ton

Art Unit: 2133

Docket No.: X-916 US

Conf. No.: 3726

Mail Stop Amendment  
COMMISSIONER FOR PATENTS  
P.O. Box 1450  
Alexandria, Virginia 22313-1450



AMENDMENT IN RESPONSE TO THE FIRST OFFICE ACTION

Dear Sir:

In response to the First Office Action mailed from the Patent Office on October 1, 2004, please replace/substitute the following claims as indicated.

**Amendments to the Claims** begin on page 2 of this paper.

**Remarks** begin on page 9 of this paper.

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01 FC:1201 200.00 DA  
02 FC:1202 200.00 DA